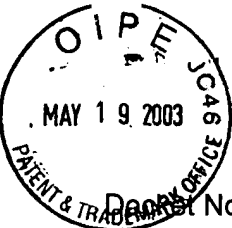


2811 / \$



Patent No.: IRV1.PAU.53

Patent Application

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Pepe, et al.

Examiner: Vu, Hung K.

Serial No.: 09/938,686

Art Unit: 2811

Filed: October 30, 2001

Title: STACKABLE LAYERS CONTAINING  
ENCAPSULATED INTEGRATED  
CIRCUIT CHIPS WITH ONE OR  
MORE OVERLYING INTERCONNECT  
LAYERS AND A METHOD OF  
MAKING THE SAME

May 15, 2003

Irvine, California

#9Kpt  
Cm  
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REQUEST FOR TWO-MONTH EXTENSION OF TIME

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicant requests a two-month extension of time from March 19, 2003 to May 19, 2003 to respond to the Office Action of December 19, 2002.

Our check in the amount of \$205.00 to cover the fee for such an extension of time is enclosed. Applicant is entitled to small entity status.

Please charge any additional fees to our Deposit Account No. 01-1960. One copy of this letter is enclosed for such purpose.

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 15, 2003.

By Angela Williams

Signature AW  
May 15, 2003

Respectfully submitted,

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